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| ;========================================…………….============================  Nama : Giri Atma Santana  NRP : 0711540000118  ; Main.asm file generated by New Project wizard  ;  ; Created: Wed Feb 20 2019  ; Processor: AT89C51  ; Compiler: ASEM-51 (Proteus)  ;====================================================================  $NOMOD51  $INCLUDE (8051.MCU)  ;====================================================================  ; DEFINITIONS  ;====================================================================  ;====================================================================  ; VARIABLES  ;====================================================================  ;====================================================================  ; RESET and INTERRUPT VECTORS  ;====================================================================  ; Reset Vector  org 0000h  jmp start  ;====================================================================  ; CODE SEGMENT  ;====================================================================  org 00h  start:  MOV TMOD,#00000001B  MULAI:  ACALL RESET\_CYCLE  DEFAULT:  DEC A  CLR P1.0  CLR P1.1  CLR P1.2  CLR P1.3  ACALL PWM\_LOW\_0  ACALL PWM\_HIGH\_0  JZ NEXT  SJMP DEFAULT    NEXT:  ACALL RESET\_CYCLE  KANAN\_DEPAN\_1 :  DEC A  SETB P1.1  ACALL PWM\_LOW\_N45  CLR P1.1  ACALL PWM\_HIGH\_N45  JZ NEXT\_1  SJMP KANAN\_DEPAN\_1  NEXT\_1:  ACALL RESET\_CYCLE  KANAN\_DEPAN\_2:  DEC A  SETB P1.0  ACALL PWM\_LOW\_0  CLR P1.0  ACALL PWM\_HIGH\_0  JZ NEXT\_3  SJMP KANAN\_DEPAN\_2  NEXT\_3:  ACALL RESET\_CYCLE  KANAN\_DEPAN\_3:  DEC A  SETB P1.1  ACALL PWM\_LOW\_45  CLR P1.1  ACALL PWM\_HIGH\_45  JZ NEXT\_4  SJMP KANAN\_DEPAN\_3    ;==========================================  NEXT\_4:  ACALL RESET\_CYCLE  KANAN\_BELAKANG\_1 :  DEC A  SETB P1.3  ACALL PWM\_LOW\_N45  CLR P1.3  ACALL PWM\_HIGH\_N45  JZ NEXT\_5  SJMP KANAN\_BELAKANG\_1  NEXT\_5:  ACALL RESET\_CYCLE  KANAN\_BELAKANG\_2:  DEC A  SETB P1.2  ACALL PWM\_LOW\_0  CLR P1.2  ACALL PWM\_HIGH\_0  JZ NEXT\_6  SJMP KANAN\_BELAKANG\_2  NEXT\_6:  ACALL RESET\_CYCLE  KANAN\_BELAKANG\_3:  DEC A  SETB P1.3  ACALL PWM\_LOW\_45  CLR P1.3  ACALL PWM\_HIGH\_45  JZ NEXT\_7  SJMP KANAN\_BELAKANG\_3  ;==================================  NEXT\_7:  ACALL RESET\_CYCLE  KIRI\_DEPAN\_1 :  DEC A  SETB P1.5  ACALL PWM\_LOW\_N45  CLR P1.5  ACALL PWM\_HIGH\_N45  JZ NEXT\_8  SJMP KIRI\_DEPAN\_1  NEXT\_8:  ACALL RESET\_CYCLE  KIRI\_DEPAN\_2:  DEC A  SETB P1.4  ACALL PWM\_LOW\_0  CLR P1.4  ACALL PWM\_HIGH\_0  JZ NEXT\_9  SJMP KIRI\_DEPAN\_2  NEXT\_9:  ACALL RESET\_CYCLE  KIRI\_DEPAN\_3:  DEC A  SETB P1.5  ACALL PWM\_LOW\_45  CLR P1.5  ACALL PWM\_HIGH\_45  JZ NEXT\_10  SJMP KIRI\_DEPAN\_3  ;=========================================  NEXT\_10:  ACALL RESET\_CYCLE  KIRI\_BELAKANG\_1 :  DEC A  SETB P1.5  ACALL PWM\_LOW\_N45  CLR P1.5  ACALL PWM\_HIGH\_N45  JZ NEXT\_11  SJMP KIRI\_BELAKANG\_1  NEXT\_11:  ACALL RESET\_CYCLE  KIRI\_BELAKANG\_2:  DEC A  SETB P1.4  ACALL PWM\_LOW\_0  CLR P1.4  ACALL PWM\_HIGH\_0  JZ NEXT\_12  SJMP KIRI\_BELAKANG\_2  NEXT\_12:  ACALL RESET\_CYCLE  KIRI\_BELAKANG\_3:  DEC A  SETB P1.5  ACALL PWM\_LOW\_45  CLR P1.5  ACALL PWM\_HIGH\_45  JZ LAST  SJMP KIRI\_BELAKANG\_3  ;=========================================  LAST:  ACALL RESET\_CYCLE  FINAL:  DEC A  SETB P1.0  ACALL PWM\_LOW\_N45  CLR P1.0  ACALL PWM\_HIGH\_N45  JZ MULAI  SJMP FINAL    RESET\_CYCLE:  MOV A, #0BBH  RET  PWM\_LOW\_45:  MOV TH0, #HIGH(-1700)  MOV TL0, #LOW(-1700)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_HIGH\_45:  MOV TH0, #HIGH(-18300)  MOV TL0, #LOW(-18300)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_LOW\_0:  MOV TH0, #HIGH(-500)  MOV TL0, #LOW(-500)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_HIGH\_0:  MOV TH0, #HIGH(-19400)  MOV TL0, #LOW(-19400)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_LOW\_90:  MOV TH0, #HIGH(-2200)  MOV TL0, #LOW(-2200)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_HIGH\_90:  MOV TH0, #HIGH(-16000)  MOV TL0, #LOW(-16000)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_LOW\_N45:  MOV TH0, #HIGH(-2250)  MOV TL0, #LOW(-2250)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  PWM\_HIGH\_N45:  MOV TH0, #HIGH(-16000)  MOV TL0, #LOW(-16000)  SETB TR0  JNB TF0,$  CLR TR0  CLR TF0  RET  END |